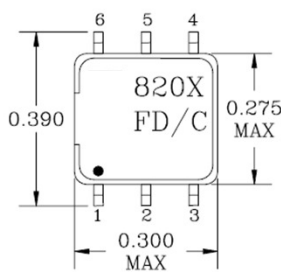


T3/DS3/E3/STS-1 Input/Output Transformers 8200-8202

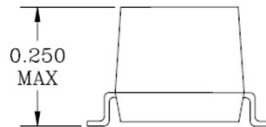
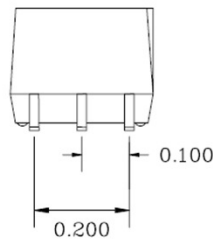
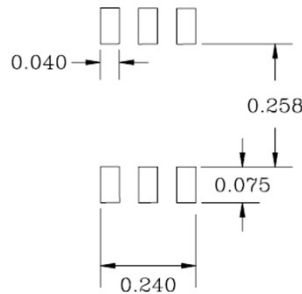
- Models matched to leading transceiver IC's
- Return Loss requirements of G.703 exceeded
- 1500 Vrms isolation per UL1459 and UL 1950
- Extended operating temperature range of -40°C to +85°C



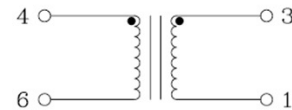
Mechanical and Schematic (All dimensions in inches)



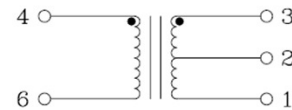
SUGGESTED PAD LAYOUT



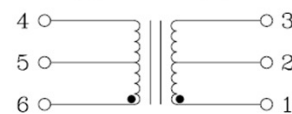
SCHEMATIC 1



SCHEMATIC 2



SCHEMATIC 3



Unless otherwise specified, all tolerances are ± 0.010

ELECTRICAL SPECIFICATION @ 25°C

Part Number	Turns Ratio ($\pm 2\%$)	OCL Primary @ 25°C ($\mu\text{H MIN}$)	OCL Primary @ -40°C ($\mu\text{H MIN}$)	L_L ($\mu\text{H MAX}$)	$C_{w/w}$ (pF MAX)	Bandwidth 75ohm System (MHz TYP)	Isolation Voltage (Vrms MIN)	Primary Pins	Schematic
8200	1:1	100	40	0.110	10	0.200 - 340	1500	1-3	1
8201	1:2CT	40	19	0.110	12	0.250 - 500	1500	4-6	2
8202	1CT:1CT	40	19	0.100	10	0.200 - 340	1500	1-3	3